WEST Search History

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DATE: Thursday, June 24, 2004

Hide?	<u>Set</u> Name	Query	<u>Hit</u> Count
	DB=US	SPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
	L5	L2 same (processor near3 memory)	8
	L4	L2 same (memory or cache)	29
	L3	L2 same memory	29
	L2	L1 same (((low\$5 or reduc\$4) adj power\$4) or (power\$4 adj down) or sleep or standby)	42
	L1	(sav\$4 near2 process\$4 near2 state)	612

END OF SEARCH HISTORY

T	TT-2 14	Coangh Mout	T nn	I m :
L Number	Hits	Search Text	DB	Time stamp
-	564	(power\$3 near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23 12:56
_	74	((power\$3 near3 cache) same (processor or	USPAT;	2004/06/23
		cpu)) and 713/3\$.ccls.	US-PGPUB	10:59
-	49	((power\$3 near3 cache) same (processor or cpu)) and 713/32\$.ccls.	USPAT;	2004/06/23
-	3	((second adj1 voltage) near3 cache) same	US-PGPUB USPAT;	11:03
		(processor or cpu)	US-PGPUB	12:54
-	372	((suppl\$3 near3 (voltage or power)) near3 cache) same (processor or cpu)	USPAT; US-PGPUB	2004/06/23
_	6	((supp1\$3 adj1 (voltage or power)) near3	USPAT;	11:02 2004/06/23
		cache) same (processor or cpu)	US-PGPUB	11:02
-	3	(((supp1\$3 near3 (voltage or power)) near3 cache) same (processor or cpu)) and	USPAT; US-PGPUB	2004/06/23
		713/32\$.cc1s.	03-PGP0B	11:03
****	11	(((suppl\$3 near3 (voltage or power))	USPAT;	2004/06/23
		near3 cache) same (processor or cpu)) and 713/3\$.ccls.	US-PGPUB	11:03
-	569	(second adj1 cache) same (processor or	USPAT;	2004/06/23
	_	cpu)	US-PGPUB	15:48
-	6	((second adj1 cache) same (processor or cpu)) and 713/32\$.ccls.	USPAT; US-PGPUB	2004/06/23 12:54
-	26	((power\$3 adj1 down) near3 cache) same	USPAT;	2004/06/23
	200	(processor or cpu)	US-PGPUB	14:45
-	396	(back\$lup or backup) near3 cache	USPAT; US-PGPUB	2004/06/23
-	3	((back\$1up or backup) near3 cache) and	USPAT;	13:11 2004/06/23
-		713/32\$.ccls.	US-PGPUB	13:03
-	97	((back\$lup or backup) near3 cache) with (processor or cpu)	USPAT;	2004/06/23
-	7	((battery) near3 cache) with (processor	US-PGPUB USPAT;	13:11 2004/06/23
		or cpu)	US-PGPUB	13:14
-	0	((auxilliary) near3 cache) with (processor or cpu)	USPAT;	2004/06/23
-	32	((aux\$8) near3 cache) with (processor or	US-PGPUB USPAT;	13:15 2004/06/23
	-	cpu)	US-PGPUB	13:20
-	1	((aux\$8 adj2 power) with cache) with (processor or cpu)	USPAT; US-PGPUB	2004/06/23
-	5	((battery near3 (backup or back-up))	USPAT;	2004/06/23
_	408	near3 cache) with (processor or cpu) (second adj1 cache) with (processor or	US-PGPUB	13:20
	400	cpu)	USPAT; US-PGPUB	2004/06/23
j-	0	(second adj1 cache) with(processor or	USPAT;	2004/06/23
_	2	<pre>cpu) with battery (second adj1 cache) with(processor or</pre>	US-PGPUB	13:22
	۷	cpu) with voltage	USPAT; US-PGPUB	2004/06/23
-	0	((second adj1 cache) with (processor or	USPAT;	2004/06/23
_	o	<pre>cpu)) with battery (integrated near3 (back-up or backup adj2</pre>	US-PGPUB USPAT;	13:42 2004/06/23
		cache)) same (processor or cpu)	US-PGPUB	13:43
-	52	(integrated near3 (back-up or backup adj2	USPAT;	2004/06/23
-	0	<pre>cache)) (integrated near3 ((back-up or backup)</pre>	US-PGPUB USPAT;	13:44
		adj2 cache))	US-PGPUB	13:45
~	85	(second adj1 voltage) near5 (input\$3 or	USPAT;	2004/06/23
-	7	<pre>supp1\$4) with (processor or cpu) ((second adj1 voltage) near5 (input\$3 or</pre>	US-PGPUB USPAT;	13:46 2004/06/23
		supp1\$4) with (processor or cpu)) and	US-PGPUB	13:49
_	3	713/32\$.ccls. ((stor\$3 or sav\$3) near3 context) with	HCD2m.	2004/05/03
	3	processor with (sleep or standby or	USPAT; US-PGPUB	2004/06/23 13:56
		stand-by)		
_	49	(cache) with processor with (sleep or standby or stand-by)	USPAT; US-PGPUB	2004/06/23
-	17	((cache) with processor with (sleep or	USPAT;	2004/06/23
	24	standby or stand-by)) and 713/3\$.ccls.	US-PGPUB	13:52
-	34	((stor\$3 or sav\$3) near3 state) with processor with (s1eep or standby or	USPAT; US-PGPUB	2004/06/23 13:57
		stand-by)		10.07

_	25	1 , , , , , , , , , , , , , , , , , , ,	USPAT;	2004/06/23
		cpu) near3 state) with (sleep or standby	US-PGPUB	13:59
		or stand-by)		
-	2	(power\$3 near2 cache) with (processor or	USPAT;	2004/06/23
		cpu) with (sleep or standby or stand-by)	US-PGPUB	14:22
-	2476	(processor or cpu) with (integrated near2	USPAT;	2004/06/23
		memory)	US-PGPUB	14:23
-	10	((processor or cpu) with (integrated	USPAT;	2004/06/23
		near2 memory)) and 713/32\$.ccls.	US-PGPUB	14:23
-	1	((constant\$3 or always) near3 power\$3	USPAT;	2004/06/23
		near3 cache) same (processor or cpu)	US-PGPUB	14:50
-	1139	(internal near2 cache) with (cpu or	USPAT;	2004/06/23
		processor)	US-PGPUB	14:51
-	9	((internal near2 cache) with (cpu or	USPAT;	2004/06/23
İ	İ	processor)) with (sleep or standby or	US-PGPUB	15:27
		stand-by or (low adjl power) or		
		low-power)		
-	10	((separat\$4 near2 power\$3) near6 (cache))	USPAT;	2004/06/23
		same (processor or cpu)	US-PGPUB	15:28
-	26	((separat\$4 near2 power\$3) near6 (cache))	USPAT;	2004/06/23
			US-PGPUB	15:30
_	1	6738675.pn.	USPAT;	2004/06/23
-			US-PGPUB	15:30
-	17	("5021679" "5153535" "5307003"	USPAT	2004/06/23
		"5513358" "5625806" "5627412"		15:30
		"5752011" "5813022" "5963023"		
		"5974557" "6108226" "6141765"		
		"6216235" "6298105" "6347379"		
		"6351150" "6487668").PN.		
_	11	5627412.URPN.	USPAT	2004/06/23
				15:32
-	43	(second adj1 cache) same (processor or	EPO; JPO;	2004/06/23
		cpu)	IBM TDB	15:49
_	3	5918056.pn. 5784628.pn. 5652890.pn.	USPĀT	2004/06/24
				14:07